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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **A 0.5-V 1-/spl mu/W successive approximation ADC**
 Sauerbrey, J.; Schmitt-Landsiedel, D.; Thewes, R.;
 Solid-State Circuits, IEEE Journal of
 Volume 38, Issue 7, July 2003 Page(s):1261 - 1265
 Digital Object Identifier 10.1109/JSSC.2003.813217
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(411 KB) IEEE JNL
- ☐ 2. **A 0.5V, 1μW successive approximation ADC**
 Sauerbrey, J.; Schmitt-Landsiedel, D.; Thewes, R.;
 Solid-State Circuits Conference, 2002. ESSCIRC 2002. Proceedings of the 28th
 24-26 Sept. 2002 Page(s):247 - 250
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) IEEE CNF
- ☐ 3. **A 1.2V 10b 20MSample/s non-binary successive approximation ADC in 0.18μm CMOS**
 Kuttner, F.;
 Solid-State Circuits Conference, 2002. Digest of Technical Papers. ISSCC. 2002
 International
 Volume 1, 3-7 Feb. 2002 Page(s):176 - 177 vol.1
 Digital Object Identifier 10.1109/ISSCC.2002.992993
[AbstractPlus](#) | Full Text: [PDF](#)(337 KB) IEEE CNF
- ☐ 4. **A successive approximation A/D converter with 16 bit 200 kS/s in 0.6 μm CMOS**
 Neubauer, H.; Desel, T.; Hauer, H.;
 Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International
 on
 Volume 2, 2-5 Sept. 2001 Page(s):859 - 862 vol.2
 Digital Object Identifier 10.1109/ICECS.2001.957609
[AbstractPlus](#) | Full Text: [PDF](#)(320 KB) IEEE CNF
- ☐ 5. **An 8-b 1.3-MHz successive-approximation A/D converter**
 Hadidi, K.; Tso, V.S.; Temes, G.C.;
 Solid-State Circuits, IEEE Journal of
 Volume 25, Issue 3, Jun 1990 Page(s):880 - 885
 Digital Object Identifier 10.1109/4.102691
[AbstractPlus](#) | Full Text: [PDF](#)(476 KB) IEEE JNL

- ☐ **6. A 1-V, 8-bit successive approximation ADC in standard CMOS process**
Mortezapour, S.; Lee, E.K.F.;
Solid-State Circuits, IEEE Journal of
Volume 35, Issue 4, April 2000 Page(s):642 - 646
Digital Object Identifier 10.1109/4.839925
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(188 KB) IEEE JNL

- ☐ **7. 12-bit low-power fully differential switched capacitor noncalibrating successive approximation ADC with 1 MS/s**
Promitzer, G.;
Solid-State Circuits, IEEE Journal of
Volume 36, Issue 7, July 2001 Page(s):1138 - 1143
Digital Object Identifier 10.1109/4.933473
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(152 KB) IEEE JNL

- ☐ **8. A 32-channel, 0.25 /spl mu/m CMOS ASIC for the readout of the silicon drift detector for the ALICE experiment**
Mazza, G.; Rivetti, A.; Anelli, G.; Anghinolfi, F.; Martinez, M.I.; Rotondo, F.;
Nuclear Science, IEEE Transactions on
Volume 51, Issue 5, Part 1, Oct. 2004 Page(s):1942 - 1947
Digital Object Identifier 10.1109/TNS.2004.834716
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(648 KB) IEEE JNL

- ☐ **9. Modelling ADC nonlinearity in Monte Carlo procedures for uncertainty estimation**
Locci, N.; Muscas, C.; Sulis, S.;
Instrumentation and Measurement Technology Conference, 2004. IMTC 04. Proceedings of the
Volume 1, 18-20 May 2004 Page(s):522 - 527 Vol.1
Digital Object Identifier 10.1109/IMTC.2004.1351102
[AbstractPlus](#) | Full Text: [PDF](#)(497 KB) IEEE CNF

- ☐ **10. Low thermal error sampling comparator for accurate settling time measurement**
Bergman, D.I.; Waltrip, B.C.;
Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on
Volume 1, 23-26 May 2004 Page(s):521 - 524 Vol.1
Digital Object Identifier 10.1109/ISCAS.2004.1328246
[AbstractPlus](#) | Full Text: [PDF](#)(458 KB) IEEE CNF

- ☐ **11. Techniques to improve linearity of CMOS sample-and-hold circuits for accurate performance at 80 MSps**
Tadeparth, P.; Das, M.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 5, 26-29 May 2002 Page(s):V-581 - V-584 vol.5
Digital Object Identifier 10.1109/ISCAS.2002.1010770
[AbstractPlus](#) | Full Text: [PDF](#)(379 KB) IEEE CNF

- ☐ **12. Low power pipelined successive approximation A/D converter**
Shouli Yan; Maloberti, F.; Jinghua Li;
Low Power/Low Voltage Mixed-Signal Circuits and Systems, 2001. (DCAS-01) Proceedings of the
the IEEE 2nd Dallas CAS Workshop on
26 March 2001 Page(s):P1 - P2
Digital Object Identifier 10.1109/DCAS.2001.920981
[AbstractPlus](#) | Full Text: [PDF](#)(136 KB) IEEE CNF

- ☐ **13. Optimal sampling strategies for learning a fitness model**
Ratle, A.;
Evolutionary Computation, 1999. CEC 99. Proceedings of the 1999 Congress on
Volume 3, 6-9 July 1999 Page(s):
Digital Object Identifier 10.1109/CEC.1999.785531

[AbstractPlus](#) | Full Text: [PDF](#)(648 KB) IEEE CNF

- ☐ **14. Constant-rate wavelet-based image compressor (CWIC)**
Schaefer, C.; Krahn, E.;
Geoscience and Remote Sensing Symposium, 1999. IGARSS '99 Proceeding:
International
Volume 4, 28 June-2 July 1999 Page(s):2258 - 2260 vol.4
Digital Object Identifier 10.1109/IGARSS.1999.775094
[AbstractPlus](#) | Full Text: [PDF](#)(316 KB) IEEE CNF

- ☐ **15. A new dual-mode data compressing A/D converter**
Lampinen, H.; Vainio, O.;
Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE Internation
Volume 1, 9-12 June 1997 Page(s):429 - 432 vol.1
Digital Object Identifier 10.1109/ISCAS.1997.608759
[AbstractPlus](#) | Full Text: [PDF](#)(508 KB) IEEE CNF

- ☐ **16. A novel algorithm for precision RMS measurement of VLF voltage**
Chao Shinmin; Li Desheng;
Precision Electromagnetic Measurements, 1990. CPEM '90 Digest., Conferenc
11-14 June 1990 Page(s):380 - 381
Digital Object Identifier 10.1109/CPEM.1990.110069
[AbstractPlus](#) | Full Text: [PDF](#)(100 KB) IEEE CNF

- ☐ **17. Single event effects testing of the Crystal CS5327 16-bit ADC**
McCarty, K.P.; Coss, J.R.; Nichols, D.K.; Swift, G.M.; LaBel, K.A.;
Radiation Effects Data Workshop, 1994 IEEE
20 July 1994 Page(s):86 - 96
Digital Object Identifier 10.1109/REDW.1994.633040
[AbstractPlus](#) | Full Text: [PDF](#)(468 KB) IEEE CNF

- ☐ **18. Low power 13 bit ADCs for ASIC applications**
Hopper, A.;
Advanced A-D and D-A Conversion Techniques and their Applications, 1994. I
International Conference on
6-8 Jul 1994 Page(s):90 - 95
[AbstractPlus](#) | Full Text: [PDF](#)(368 KB) IEE CNF

- ☐ **19. Statistical analysis of network traffic for adaptive faults detection**
Hajji, H.;
Neural Networks, IEEE Transactions on
Volume 16, Issue 5, Sept. 2005 Page(s):1053 - 1063
Digital Object Identifier 10.1109/TNN.2005.853414
[AbstractPlus](#) | Full Text: [PDF](#)(568 KB) IEEE JNL

- ☐ **20. Investigation of an efficient representation of speech spectra for segmen
classification of speech sounds**
Beninghof, W., Jr.; Ross, M.;
Audio and Electroacoustics, IEEE Transactions on
Volume 18, Issue 1, Mar 1970 Page(s):33 - 42
[AbstractPlus](#) | Full Text: [PDF](#)(832 KB) IEEE JNL

- ☐ **21. Synthesis of shaped-beam radiation patterns using the iterative sampling**
Stutzman, W.;
Antennas and Propagation, IEEE Transactions on [legacy, pre - 1988]
Volume 19, Issue 1, Jan 1971 Page(s):36 - 41
[AbstractPlus](#) | Full Text: [PDF](#)(584 KB) IEEE JNL

- ☐ **22. Sequential M-ary PAM System**
Nishikawa, S.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 21, Issue 1, Jan 1973 Page(s):22 - 33
[AbstractPlus](#) | Full Text: [PDF](#)(944 KB) IEEE JNL
- ☐ **23. On Sequential M-ary Orthogonal Modulation**
Nishikawa, S.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 21, Issue 10, Oct 1973 Page(s):1100 - 1108
[AbstractPlus](#) | Full Text: [PDF](#)(736 KB) IEEE JNL
- ☐ **24. High-resolution A/D conversion in MOS/LSI**
Fotouhi, B.; Hodges, D.A.;
Solid-State Circuits, IEEE Journal of
Volume 14, Issue 6, Dec 1979 Page(s):920 - 926
[AbstractPlus](#) | Full Text: [PDF](#)(1376 KB) IEEE JNL
- ☐ **25. A monolithic charge-balancing successive approximation A/D technique**
Redfern, T.P.; Connolly, J.J.; Chin, S.W.; Frederiksen, T.M.;
Solid-State Circuits, IEEE Journal of
Volume 14, Issue 6, Dec 1979 Page(s):912 - 920
[AbstractPlus](#) | Full Text: [PDF](#)(1496 KB) IEEE JNL

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1 [VLSI design: Design and modeling of a 16-bit 1.5MSPS successive approximation](#)

[ADC with non-binary capacitor array](#)

Jianhua Gan, Shouli Yan, Jacob Abraham

 April 2003 **Proceedings of the 13th ACM Great Lakes symposium on VLSI**

 Full text available: pdf(125.17 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The design and modeling of a high performance successive approximation analog-to-digital converter (ADC) using non-binary capacitor array are presented in this paper. A non-binary capacitor array with 20 capacitors is used to design a 16-bit, 1.5 mega samples per second (MSPS) successive approximation ADC. A perceptron learning rule, originally developed for Artificial Intelligence applications, is used as the capacitor calibration algorithm. The system architecture and the circuit design for th ...

Keywords: analog-to-digital converter, calibration, non-binary capacitor array, successive approximation

2 [Analog design and evaluation: Effects of noise and nonlinearity on the calibration of a non-binary capacitor array in a successive approximation analog-to-digital converter](#)

Jianhua Gan, Shouli Yan, Jacob Abraham

 January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04 , Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04**

Full text available: pdf(113.94 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#)


[Publisher Site](#)

A successive approximation analog-to-digital converter using a non-binary capacitor array is presented. A perceptron learning rule is used as the capacitor calibration algorithm. The nonlinearity is analyzed using the Volterra series. The effects of noise and nonlinearity are modeled to verify the calibration robustness. With the presence of noise and nonlinearity, the capacitor weights are adaptively calibrated to match the physical capacitors with better than 22-bit accuracy. The accuracy is n ...

3 [Approximating optimal spare capacity allocation by successive survivable routing](#)

Yu Liu, David Tipper, Peerapon Siripongwutikorn

 February 2005 **IEEE/ACM Transactions on Networking (TON)**, Volume 13 Issue 1

Full text available:  pdf(1.27 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The design of survivable mesh based communication networks has received considerable attention in recent years. One task is to route backup paths and allocate spare capacity in the network to guarantee seamless communications services survivable to a set of failure scenarios. This is a complex multi-constraint optimization problem, called the spare capacity allocation (SCA) problem. This paper unravels the SCA problem structure using a matrix-based model, and develops a fast and efficient approx ...

Keywords: MPLS traffic engineering, multi-commodity flow, network planning and optimization, network survivability, protection and restoration, spare capacity allocation, survivable routing

4 [Session 5A: Approximate clustering via core-sets](#)

Mihai Bădoiu, Sariel Har-Peled, Piotr Indyk

May 2002 **Proceedings of the thirty-fourth annual ACM symposium on Theory of computing**


Full text available:  pdf(183.97 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In this paper, we show that for several clustering problems one can extract a small set of points, so that using those *core-sets* enable us to perform approximate clustering efficiently. The surprising property of those core-sets is that their size is independent of the dimension. Using those, we present a $(1 + \epsilon)$ -approximation algorithms for the k -center clustering and k -median clustering problems in Euclidean space. The running time of the new algorithms has linear or near ...

5 [Random sampling for histogram construction: how much is enough?](#)

Surajit Chaudhuri, Rajeev Motwani, Vivek Narasayya

June 1998 **ACM SIGMOD Record , Proceedings of the 1998 ACM SIGMOD international conference on Management of data**, Volume 27 Issue 2


Full text available:  pdf(1.73 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Random sampling is a standard technique for constructing (approximate) histograms for query optimization. However, any real implementation in commercial products requires solving the hard problem of determining "How much sampling is enough?" We address this critical question in the context of equi-height histograms used in many commercial products, including Microsoft SQL Server. We introduce a conservative error metric capturing the intuition that ...

6 [Approximate medians and other quantiles in one pass and with limited memory](#)

Gurmeet Singh Manku, Sridhar Rajagopalan, Bruce G. Lindsay

June 1998 **ACM SIGMOD Record , Proceedings of the 1998 ACM SIGMOD international conference on Management of data**, Volume 27 Issue 2

Full text available:  pdf(1.25 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present new algorithms for computing approximate quantiles of large datasets in a single pass. The approximation guarantees are explicit, and apply for arbitrary value distributions and arrival distributions of the dataset. The main memory requirements are smaller than those reported earlier by an order of magnitude. We also discuss methods that couple the approximation algorithms with random sampling to further reduce memory requirements. With sampling, the approximation guar ...

7

[Data streams: Approximate counts and quantiles over sliding windows](#)

Arvind Arasu, Gurmeet Singh Manku

June 2004 **Proceedings of the twenty-third ACM SIGMOD-SIGACT-SIGART symposium on Principles of database systems PODS '04**

Full text available:  pdf(204.76 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

We consider the problem of maintaining ϵ -approximate counts and quantiles over a stream *sliding window* using limited space. We consider two types of sliding windows depending on whether the number of elements N in the window is fixed (*fixed-size* sliding window) or variable (*variable-size* sliding window). In a fixed-size sliding window, both the ends of the window slide synchronously over the stream. In a variable-size sliding window, an adversary slides the wi ...

8 Session 11B: Efficient sequences of trials

Edith Cohen, Amos Fiat, Haim Kaplan

January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms**

Full text available:  pdf(873.28 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We introduce a problem called *sequential trial optimization*, a generalization of the well studied set cover problem with a new objective function. We give a simple algorithm that achieves a constant factor approximation to this problem. Sequential trial optimization naturally arises in heterogenous search environments such as peer to peer networks.

9 Monte Carlo summation and integration applied to multiclass queuing networks

Keith W. Ross, Danny H. K. Tsang, Jie Wang

November 1994 **Journal of the ACM (JACM)**, Volume 41 Issue 6

Full text available:  pdf(1.64 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Although many closed multiclass queuing networks have a product-form solution, evaluating their performance measures remains nontrivial due to the presence of a normalization constant. We propose the application of Monte Carlo summation in order to determine the normalization constant, throughputs, and gradients of throughputs. A class of importance-sampling functions leads to a decomposition approach, where separate single-class problems are first solved in a setup module, and then the ori ...

Keywords: gradient estimation, product-form queuing networks, variation reduction

10 Piecewise surface flattening for non-distorted texture mapping

Chakib Bennis, Jean-Marc Vézien, Gérard Iglésias

July 1991 **ACM SIGGRAPH Computer Graphics , Proceedings of the 18th annual conference on Computer graphics and interactive techniques**, Volume 25 Issue 4

Full text available:  pdf(4.35 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper introduces new techniques for interactive piecewise flattening of parametric 3-D surfaces, leading to a non-distorted, hence realistic, texture mapping. Cuts are allowed on the mapped texture and we make a compromise between discontinuities and distortions. These techniques are based on results from differential geometry, more precisely on the notion of "**geodesic curvature**": isoparametric curves of the surface are mapped, in a constructive way, onto curves in the texture plane ...

Keywords: differential geometry, geodesic curvature, non distorted texture mapping, piecewise surface flattening

11 Learning efficient query processing strategies

Russell Greiner

July 1992 **Proceedings of the eleventh ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems**Full text available:  [pdf\(1.40 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A query processor QP uses the rules in a rule base to reduce a given query to a series of attempted retrievals from a database of facts. The Qp's expected cost is the average time it requires to find an answer, averaged over its anticipated set of queries. This cost depends on Qp's strategy, which specifies the order in which it considers the possible rules and retrievals. This paper provides two related learning algorithms, PIB and PAO, for improving the Q ...

12 Meshes & surfaces: Adaptive sampling of intersectable models exploiting image and object-space coherence

Anders Adamson, Marc Alexa, Andrew Nealen

April 2005 **Proceedings of the 2005 symposium on Interactive 3D graphics and games**Full text available:  [pdf\(6.01 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We present a sampling strategy and rendering framework for intersectable models, whose surface is implicitly defined by a black box intersection test that provides the location and normal of the closest intersection of a ray with the surface. To speed up image generation despite potentially slow intersection tests, our method exploits spatial coherence by adjusting the sampling resolution in image space to the surface variation in object space. The result is a set of small, view-dependent biline ...

Keywords: adaptive sampling, object-space coherence**13 Uniform generation in spatial constraint databases and applications (Extended abstract)**

David Gross, Michel de Rougemont

May 2000 **Proceedings of the nineteenth ACM SIGMOD-SIGACT-SIGART symposium on Principles of database systems**Full text available:  [pdf\(205.43 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We study the efficient approximation of queries in linear constraint databases using sampling techniques. We define the notion of an almost uniform generator for a generalized relation and extend the classical generator of Dyer, Frieze and Kannan for convex sets to the union and the projection of relations. For the intersection and the difference, we give sufficient conditions for the existence of such generators. We show how such generators give relative estimations of the volume and appro ...


14 High quality rendering of attributed volume data

Ulf Tiede, Thomas Schiemann, Karl Heinz Höhne

October 1998 **Proceedings of the conference on Visualization '98**Full text available:  [pdf\(2.17 MB\)](#)  Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)**Keywords:** partial-volume-effect, ray-casting, tomographic data, visible-human-project**15 Research sessions: query processing I: A scalable hash ripple join algorithm**

Gang Luo, Curt J. Ellmann, Peter J. Haas, Jeffrey F. Naughton

June 2002 **Proceedings of the 2002 ACM SIGMOD international conference on Management of data**

Full text available:  pdf(1.12 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Recently, Haas and Hellerstein proposed the hash ripple join algorithm in the context of online aggregation. Although the algorithm rapidly gives a good estimate for many join-aggregate problem instances, the convergence can be slow if the number of tuples that satisfy the join predicate is small or if there are many groups in the output. Furthermore, if memory overflows (for example, because the user allows the algorithm to run to completion for an exact answer), the algorithm degenerates to bl ...

16 A fast and accurate framework to analyze and optimize cache memory behavior

Xavier Vera, Nerina Bermudo, Josep Llosa, Antonio González

March 2004 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 26 Issue 2

Full text available:  pdf(270.06 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

The gap between processor and main memory performance increases every year. In order to overcome this problem, cache memories are widely used. However, they are only effective when programs exhibit sufficient data locality. Compile-time program transformations can significantly improve the performance of the cache. To apply most of these transformations, the compiler requires a precise knowledge of the locality of the different sections of the code, both before and after being transformed. Cache ...

Keywords: Cache memories, optimization, sampling

17 Session P3: filtering and sampling: Undersampling and oversampling in sample based shape modeling

Tamal K. Dey, Joachim Giesen, Samrat Goswami, James Hudson, Raphael Wenger, Wulue Zhao

October 2001 **Proceedings of the conference on Visualization '01**

Full text available:  pdf(5.07 MB) 
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Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Shape modeling is an integral part of many visualization problems. Recent advances in scanning technology and a number of surface reconstruction algorithms have opened up a new paradigm for modeling shapes from samples. Many of the problems currently faced in this modeling paradigm can be traced back to two anomalies in sampling, namely *undersampling* and *oversampling*. Boundaries, non-smoothness and small features create undersampling problems, whereas oversampling leads to too many ...

Keywords: computational geometry, geometric modeling, mesh generation, polygonal mesh reduction, polygonal modeling, shape recognition, surface reconstruction

18 Temporal anti-aliasing in computer generated animation

Jonathan Korein, Norman Badler

July 1983 **ACM SIGGRAPH Computer Graphics , Proceedings of the 10th annual conference on Computer graphics and interactive techniques**, Volume 17 Issue 3

Full text available:  pdf(1.09 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The desirability of incorporating temporal anti-aliasing, or motion blur, into computer generated animation is discussed and two algorithms for achieving this effect are described.

The first approximates continuous object movement and determines intervals during which each object covers each pixel. Hidden surface removal is then performed, allowing the calculation of visible object intensity functions and subsequent filtering. The second form of algorithm detailed involves supersampling the ...

19 Approximate query processing using wavelets

Kaushik Chakrabarti, Minos Garofalakis, Rajeew Rastogi, Kyuseok Shim

September 2001 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 10 Issue 2-3

Full text available:  [pdf\(390.24 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)


Approximate query processing has emerged as a cost-effective approach for dealing with the huge data volumes and stringent response-time requirements of today's decision support systems (DSS). Most work in this area, however, has so far been limited in its query processing scope, typically focusing on specific forms of aggregate queries. Furthermore, conventional approaches based on sampling or histograms appear to be inherently limited when it comes to approximating the results of complex queries ...

Keywords: Approximate query answers, Data synopses, Query processing, Wavelet decomposition

20 The JPEG still picture compression standard

Gregory K. Wallace

April 1991 **Communications of the ACM**, Volume 34 Issue 4

Full text available:  [pdf\(3.29 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

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L15	28	successive approximation converter sample hold capacitor comparator switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/10/19 09:31
L16	6	successive approximation converter sample hold capacitor comparator switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/10/19 09:31
L17	49	(341/161).CCLS.	US-PGPUB	OR	OFF	2005/10/19 09:32